IN THE UNITED STATES
PATENT AND TRADEMARK OFFICE

APPLICANTS: James D. Kelly *et al.*

APPLICATION NO.: 10/006,939

FILING DATE: November 30, 2001

TITLE: BUS TRANSACTION REORDERING IN A COMPUTER SYSTEM HAVING UNORDERED SLAVES

EXAMINER: Gopal C. Ray

GROUP ART UNIT: 2181

ATTY. DOCKET NO.: 18602-06222 (P2080R1)

CERTIFICATE OF MAILING

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed to: Box Non-Fee Amendment, Commissioner for Patents, Washington, DC 20231, on the date shown below:

Dated: December 18, 2002 By: _____

Kirk A. Gottlieb, Reg. No. 42,596

BOX NON-FEE AMENDMENT
COMMISSIONER FOR PATENTS
WASHINGTON, DC 20231

RESPONSE A

Sir:

IN THE CLAIMS

All pending claims are set forth below. No amendments have been made.

1. (Unchanged) In a computer system having a system bus and having arbitration circuitry, multiple master devices including a system microprocessor, and multiple slave devices, all coupled to the system bus, a method of reordering system bus transactions, comprising the steps of:

  receiving and queuing within a particular slave device a plurality of transactions;

  within said arbitration circuitry, arbitrating between pending transactions based on

   arbitration policies including an arbitration policy that responses are received